



SIES Graduate School of Technology  
Sri Chandrasekarendra Saraswati Vidyapuram  
Sector 5, Nerul, Navimumbai-400706



## Department of Electronics and Telecommunication

### Event Report

#### INDUSTRIAL VISIT to TIFR

Event Information
<b>Event Type:</b> Industrial Visit
<b>Event title:</b> Industrial Visit: Tata Institute of Fundamental Research (TIFR)
<b>Resource Person:</b> Dr. Satyanarayana Bheesette
<b>Event Date:</b> 21 <sup>st</sup> September, 2019
<b>Organized for:</b> Students
<b>Organized by:</b> Prof. Biju Balakrishnan, Dr.Preeti Hemnani, Pranavi Mhatre
<b>Target Audience (Branch &amp; Nos.):</b> B.E
<b>Attachments:</b> 1. Photographs (in JPEG/PNG) 2. Attendance Report&Feedback Report 3. Impact Analysis

## Event Description

The industrial visit was held on 21<sup>st</sup>September, 2019. Students were introduced to Dr. Satyanarayana Bheesette. He is the scientific officer for the Department of High Energy Physics in TIFR. Students were also introduced to Mr. Yuvraj who is working along with Satyanarayana Sir. He is an expert in FPGA designs and electronics specialist.

At our industrial visit we studied about the following points:

- Introduction to the particles that is further beyond the basic 3 elements of atom, The elementary particles such as muons, neutrinos, quarks.
- Dr. Satyanarayana Sir then explained about his field of study, The High Energy Physics and explained where are the studies are used.
- He then explained about the high energy physics detectors that were built in the 1990's used to detect the elementary particles.
- He also made us aware of the contributions of the Indian scientists to the CERN hadron collider experiments
- Iron Calorimeter (ICAL) detector, India's Neutrino Observatory project was introduced and explained. The location of the laboratory and final goals of the project were explained.
- After explaining the basics of physics, Sir then further enlightened about the need of Electronics Engineers in the FPGA field.
- The Data acquisition Systems designed for the fast recording of the outputs from the Detectors whose speed tends up to picoseconds.
- There was question and answer session to encourage students involvement on topics of VLSI like sequential circuits ,logic gates etc.
- Basics fundamentals were explained with practical example to the students.
- Yuvraj Sir further explained his contributions to the ICAL detector project.
- Sir introduced FPGA and its use in research field.
- How to start with FPGA design, and how beneficial it is to use it, why its superior compared to microprocessors
- He also showed students the boards that he designed for the muon detector of which one was a DAQ system i.e. Data acquisition system which detects the trajectory of muons passing in the free space.
- Both the teachers motivated the students by giving different facts and achievement that were perused by them in the field by them. They indeed shared their learning experiences and how they got into this field

**At around 2 pm, the visit was over.**

## 1. Photographs (in JPEG/PNG):





