



Graduate School of
Technology

SIES

RISE WITH EDUCATION

NAAC Accredited B++

NBA Accredited

This is to certify that following students have completed SDP on "FPGA Design using VHDL" from " 10th December,2019 to 27th December, 2019 " successfully.

Sr.No	Name
1	Sheelwant Abhishek
2	Vikrant Singh
3	Vaibhav Singh
4	Karuna Saravana
5	Rushikesh Devendra
6	Archil Harkhani
7	Karthik Pant
8	Naidu Priyanka
9	Meenakshi E
10	Kambri Dikshita
11	Kavitha Ashok
12	Vikrant Vishwambhar
13	Karthik
14	Gawand Prathamesh
15	Tanuj Bhoir

Dr.Preeti H
HOD, EXTC

Dr. Atul Kemkar
Principal



SIES GRADUATE SCHOOL OF TECHNOLOGY

Sri Chandrasekarendra Saraswati Vidyapeetham, Section V, Nerul, Navi Mumbai - 400 706.

Tel: 6108 2402 • Fax : 2771 8779 • E-mail : principalgst@sies.edu.in

www.siesgst.edu.in